

# Relaxing Opacity in Pessimistic Transactional Memory

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# Software Transactional Memory

```
def thread:  
    lock_a.acquire()  
    lock_b.acquire()  
    a = b  
    lock_a.release()  
    b = b + 1  
    lock_b.release()
```

```
def thread:  
    transaction.start()  
    a = b  
    b = b + 1  
    transaction.commit()
```

## Advantages:

- ease of use on top
- efficient concurrency control under the hood

# Pessimistic vs Optimistic TM

Optimistic approach

$$T_1 \llbracket r(x)1, w(x)2 \rrbracket$$

$$T_2 \llbracket r(x)1, \searrow w(x)2 \curvearrowright \rightarrow T'_2 \llbracket r(x)2, w(x)3 \rrbracket$$

# Pessimistic vs Optimistic TM

## Optimistic approach

$$\begin{array}{l} T_1 \llbracket r(x)1, w(x)2 \rrbracket \\ T_2 \llbracket r(x)1, \searrow w(x)2 \curvearrowright \rightarrow T'_2 \llbracket r(x)2, w(x)3 \rrbracket \end{array}$$

## Pessimistic approach

$$\begin{array}{l} T_1 \llbracket r(x)1, w(x)2 \rrbracket \\ T_2 \llbracket \searrow r(x)2, w(x)3 \rrbracket \end{array}$$

# Pessimistic vs Optimistic TM

## Optimistic approach

$$\begin{array}{l} T_1 \llbracket r(x)1, w(x)2 \rrbracket \\ T_2 \llbracket r(x)1, \searrow w(x)2 \curvearrowright \rightarrow T_2' \llbracket r(x)2, w(x)3 \rrbracket \end{array}$$

## Pessimistic approach

$$\begin{array}{l} T_1 \llbracket r(x)1, w(x)2 \rrbracket \\ T_2 \llbracket \searrow r(x)2, w(x)3 \rrbracket \end{array}$$

- Prevent aborts
- Tolerate high contention
- Safe for irrevocable operations

# The joys of early release

Committing conflicting transactions

Early release on last use

$$\begin{array}{l} T_1 \llbracket r(x)1, w(x)2, r(y)1, w(y)2 \rrbracket \\ T_2 \llbracket \phantom{r(x)1}, \phantom{w(x)2}, r(x)2, w(x)3 \rrbracket \end{array}$$

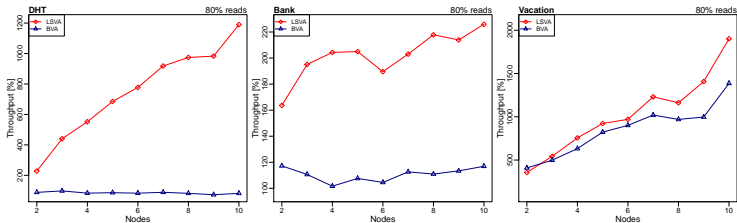
# The joys of early release

## Committing conflicting transactions

### Early release on last use

$$T_1 \llbracket r(x)1, w(x)2, r(y)1, w(y)2 \rrbracket$$
$$T_2 \llbracket \phantom{r(x)1}, \phantom{w(x)2}, \phantom{r(y)1}, w(x)3 \rrbracket$$

### Performance boost:



Siek, Wojciechowski. Atomic RMI: a Distributed Transactional Memory Framework. HLPP'14.

# Manual aborts

Cascading abort in case of arbitrary abort

$$\begin{array}{l} T_1 \llbracket r(x)1, w(x)2, r(y)1, w(y)2, \curvearrowright \\ T_2 \llbracket \quad \quad \quad \searrow r(x)2, w(x)3 \quad \quad \quad \searrow \curvearrowright \dots \end{array}$$

$T_2$  observes an **inconsistent view**  $\rightarrow$  broken invariants, segfaults, infinite looping, etc.



# Safety

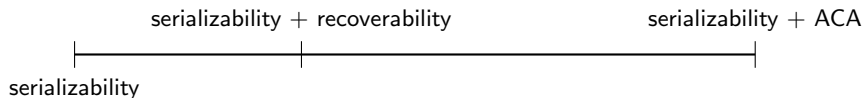
Allows reading from live transactions → **not opaque**

Precludes overwriting:

$$\begin{array}{l} T_i \llbracket w(x)0, w(x)1 \rrbracket \\ T_j \llbracket \rightarrow r(x)0 \rightarrow \circlearrowleft T'_j \llbracket r(x)1, w(x)2 \rrbracket \end{array}$$

# Safety properties for TMs with early release

- Serializability
- Elastic Opacity
- Virtual World Consistency
- TMS1 & TMS2
- Recoverability
- Avoiding Cascading Aborts
- Strictness
- Rigorousness



Siek, Wojciechowski. Zen and the Art of Concurrency Control: An Exploration of TM Safety Property Space with Early Release in Mind. WTTM'14.

# Last-use opacity

Opacity:

- Serializability
- Real-time order
- Consistency
  - read  $x$  from a committed or commit-pending transaction

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**Last-use opacity**

- Serializability
- Real-time order
- **Recoverable last-use consistency**
  - read  $x$  from a committed or commit-pending transaction **or a transaction that will no longer use  $x$**
  - commit order preserves object access order

# Characteristics

- Every LU opaque history is strict serializable, recoverable.
- Every opaque history is LU opaque.
- LU opacity prevents overwriting, allows cascading aborts.

Dziurma, Fatourou, Kanellou. Consistency for Transactional Memory Computing. EATCS Distributed Column. 2014.

Thank you