Zen and the Art of Concurrency Control An Exploration of TM Safety Property Space with Early Release in Mind

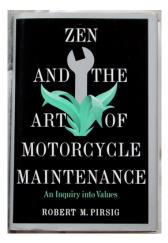
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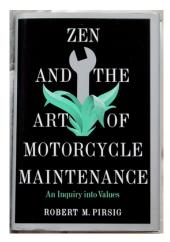




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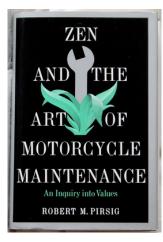


[source: wikipedia]



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Quality is not absolutely applicable \rightarrow depends on the situation



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quality = TM safety situation = high contention

High contention

```
\begin{array}{l} T_1 \left[ \begin{array}{c} r(x)0,w(x)1 \end{array} \right] \\ T_2 \left[ \begin{array}{c} r(x)0,w(x)1 \circlearrowleft \left[ \begin{array}{c} r(x)1,w(x)2 \end{array} \right] \end{array} \right. \\ T_3 \left[ \begin{array}{c} r(x)0,w(x)1 \circlearrowleft \left[ \begin{array}{c} r(x)1,w(x)2 \circlearrowleft \left[ \begin{array}{c} r(x)2,w(x)3 \end{array} \right] \end{array} \right. \\ T_4 \left[ \begin{array}{c} r(x)0,w(x)1 \circlearrowleft \left[ \begin{array}{c} r(x)1,w(x)2 \circlearrowleft \left[ \begin{array}{c} r(x)2,w(x)3 \circlearrowleft \left[ \begin{array}{c} r(x)3,w(x)4 \end{array} \right] \end{array} \right] \right. \end{array} \right. \end{array}
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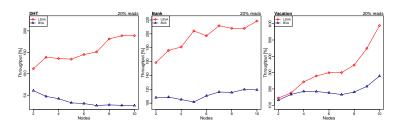
High contention

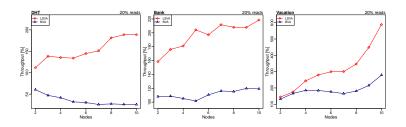
```
T_1 [ r(x)0, w(x)1 ]
T_2 \ \llbracket \ r(x)0, w(x)1 \circlearrowleft \ \llbracket \ r(x)1, w(x)2 \ \rrbracket
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T_1 [ r(x)0, w(x)1 ]
                                r(x)1, w(x)2
T_2
                                                                  \mathbf{I} \ r(x)2, w(x)3 \ \mathbf{I}
T_3
                                                                                                     r(x)3, w(x)4
T_4
```

```
T_1 \begin{bmatrix} \mathbf{r}(x)0, \ \mathbf{w}(x)1, \ \mathbf{r}(y)0, \ \mathbf{w}(y)1 \end{bmatrix}
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```

$$T_1 \begin{bmatrix} \mathbf{r}(x)0, \ \mathbf{w}(x)1, \ \mathbf{r}(y)0, \ \mathbf{w}(y)1 \ \end{bmatrix}$$

$$T_2 \qquad \qquad \begin{bmatrix} \mathbf{r}(x)1, \ \mathbf{w}(x)2, \mathbf{r}(y)1, \ \mathbf{w}(y)2 \ \end{bmatrix}$$





- M. Herlihy, V. Luchango, M. Moir, I. W. N. Scherer. Software Transactional Memory for Dynamic-sized Data Structures. PODC'03.
- H. E. Ramadan, I. Roy, M. Herlihy, E. Witchel. Committing conflicting transactions in an STM. PPoPP'09.
- P. Felber, V. Gramoli, R. Guerraoui. Elastic Transactions. DISC'09.
- A. Bieniusa, A. Middelkoop, P. Thiemann. Brief Announcement: Actions in the Twilight—Concurrent Irrevocable Transactions and Inconsistency Repair. PODC'10.
- K. Siek, P. T. Wojciechowski. Brief Announcement: Relaxing Opacity in Pessimistic Transactional Memory. DISC'14. (TBR)

Which TM safety properties can be used for early release?

Definition

Transaction T_i releases x early in H iff there is some prefix H' of H, such that T_i is live in H' and there exists T_j in H' such that there is a non-local read operation op_j in $H'|T_j$ reading v from x and a preceding write operation op_i in $H'|T_i$ writing x to v.

Example:

```
T_1 \llbracket \mathbf{r}(x)0, \ \mathbf{w}(x)\mathbf{1}, \ \mathbf{r}(y)0, \ \mathbf{w}(y)\mathbf{1} \rrbracket
T_2 \qquad \llbracket \mathbf{r}(x)\mathbf{1}, \ \mathbf{w}(x)\mathbf{2}, \mathbf{r}(y)\mathbf{1}, \ \mathbf{w}(y)\mathbf{2} \rrbracket
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History H is serializable iff there exists some linear extension (sequential witness history) \hat{S} such that \hat{S} only contains legal transactions.

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$$\hat{S} = \langle T_1, T_2 \rangle$$

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A serializable history can contain early release.

Components of opacity:

- Serializability
- Real-time order
- Consistency

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Non-local op_r in T_i ($i \neq 0$) is consistent if there is a preceding non-local write operation writing v to x in $H|T_k$ ($T_k \neq T_i$) where T_k is committed or commit-pending.

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Example:

$$\begin{array}{c|c} T_1 & \llbracket \ \mathbf{r}(y)0, \mathbf{w}(x)1, & \mathbf{r}(x)1, \mathbf{r}(y)0 \ \rrbracket \\ T_2 & \llbracket \ \mathbf{r}(x)1 \ \rrbracket \end{array} \right\} H$$

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$$\begin{array}{c|c} T_1 & \mathbb{F}(y)0, \mathbf{w}(x)1, & \mathbf{r}(x)1, \mathbf{r}(y)0 & \mathbb{F}(x)1 \\ T_2 & \mathbb{F}(x)1 & \mathbb{F}(x)1 & \mathbb{F}(x)1 \\ \hline T_1' & \mathbb{F}(y)0, \mathbf{w}(x)1 & \mathbb{F}(x)1 & \mathbb{F}(x)1 \\ T_1'' & \mathbb{F}(x)1, \mathbf{r}(y)0 & \mathbb{F}(x)1 & \mathbb{F}(x)1, \mathbf{r}(y)1 & \mathbb{F}(x)1, \mathbf{r}(y)1 \\ \hline \end{array} \right\} H$$

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An elastic opaque history can contain early release. However...

Well-formed cut:

- A subhistory cannot start with a write (unless it is the first subhistory of a cut).
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Definition (TMS1—Valid Response)

For operation op to return in some subhistory $H|T_i$, there must exist some set of transactions S that follow real-time order and justify the legality of op, and for any $T_j \in S$ it is true that,

- \blacksquare if T_j precedes T_i in real-time order then T_j is committed, or
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A TMS1 history cannot contain early release.

A TMS2 history cannot contain early release (TMS2 \subset TMS1).

Virtual world consitency

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History H is VWC iff all committed transactions are strict serializable, and for all aborted transactions there exists a linear extension of its causal past that is legal.

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■ If T_j eventually commits, then the sequential witness history $\hat{S} = \langle T_i, T_j \rangle$ is illegal.

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- If T_j eventually commits, then the sequential witness history $\hat{S} = \langle T_i, T_i \rangle$ is illegal.
- If T_j eventually aborts, its causal past $C(T_j) = \langle T_i, T_j \rangle$ contains two aborted transactions, so it is illegal.

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- Elastic opacity
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 - unintuitive cutting rules

- Recoverability
- Avoiding Cascading Aborts
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- Rigorousness

■ Recoverability

History H is recoverable iff for any $T_i, T_j \in H$ s.t. T_j reads from T_i, T_i commits in H before T_j .

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History H is strict iff for any $T_i, T_j \in H$ and given any operation $op_i = \mathbf{r}(x)v$ or $\mathbf{w}(x)v'$ in $H|T_i$, and any operation $op_j = \mathbf{w}(x)v$ in $H|T_j$, if op_i follows op_j , then T_j commits or aborts before op_i .

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■ Rigorousness

History H is rigorous if it is strict and for any $T_i, T_j \in H$ such that T_j writes to variable x, i.e., $op_j = \mathrm{w}(x)v \in H|T_j$ after T_i reads x, then T_i commits or aborts before op_j .

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■ Avoiding Cascading Aborts ≈

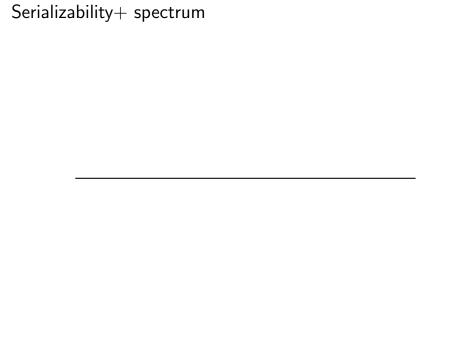
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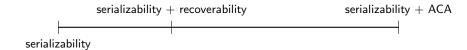
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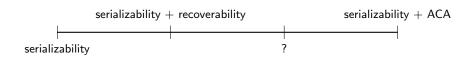
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serializability







Last-use

Definition (Commit-pending-equivalence)

A live transaction T_i in H is commit-pending-equivalent with respect to x iff it is finished executing all of its operations on x.

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```
atomic{
    int v = read(x);
    if (v < 0)
        write(x,-v); // commit-pending--equivalent wrt x
    int u = read(y);
    write(y, u + 1); // commit-pending--equivalent wrt y
}</pre>
```

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   if (v < 0)
        write(x,-v); // commit-pending--equivalent wrt x
   int u = read(y);
   write(y, u + 1); // commit-pending--equivalent wrt y
}</pre>
```

Definition (Early release after last use)

Transaction T_i releases x after last use in H iff T_i releases x early in H and T_i is commit-pending equivalent wrt x.

Last-use consistency

Definition

- Let \mathbb{T}_{er}^H be a subset of all transactions in history H that release some variable early.
- Let \mathbb{T}^H_{lu} be a subset of all transactions in history H that release some variable early only after last-use.

History H satisfies last-use consistency if $\mathbb{T}^H_{lu}=\mathbb{T}^H_{er}.$

Inconsistent views

Last-use consistency precludes overwriting:

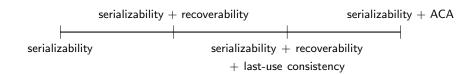
$$T_i \ \left[\begin{array}{cc} \mathbf{w}(x)0, & \mathbf{w}(x)1 \end{array} \right]$$

$$T_j \quad \left[\begin{array}{cc} \mathbf{w}(x)0 & \mathbf{w}(x)2 \end{array} \right]$$

Inconsistent views

Last-use consistency precludes overwriting:

Allowed inconsistent view:



Conclusions

- Current safety properties not enough for TM with early release
- Spectrum of database consistency properties
- Last-use consistency

Conclusions

- Current safety properties not enough for TM with early release
- Spectrum of database consistency properties
- Last-use consistency
- Future work: last-use opacity

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